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Invention: CAPPING LAYER FOR EUV OPTICAL ELEMENTS

Inventor (s): SINGH, Mandeep
VISSER, Hugo Matthieu

Pillsbury Madison & Sutro LLP
Intellectual Property Group
1100 New York Avenue, NW
Ninth Floor
Washington, DC 20005-3918
Attorneys
Telephone: (202) 861-3000

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SPECIFICATION

Capping Layer for EUV Optical Elements

*a BACK GROUND OF THE INVENTION**a 1. Field of the Invention*

5 The present invention relates to capping layers for optical elements, e.g. multilayer mirrors, for use with extreme ultraviolet (EUV) radiation. More particularly, the invention relates to the use of capping layers on optical elements in lithographic projection apparatus comprising:

- an illumination system for supplying a projection beam of radiation;
- 10 a first object table provided with a mask holder for holding a mask;
- a second object table provided with a substrate holder for holding a substrate; and
- a projection system for imaging an irradiated portion of the mask onto a target portion of the substrate.

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a 2. Description of the Related Art

For the sake of simplicity, the projection system may hereinafter be referred to as the "lens"; however, this term should be broadly interpreted as encompassing various types of projection system, including refractive optics, reflective optics, catadioptric systems, and charged particle optics, for example. The illumination system may also include elements operating according to any of these principles for directing, shaping or controlling the projection beam, and such elements may also be referred to below, collectively or singularly, as a "lens". In addition, the first and second object tables may be referred to as the "mask-table" and the "substrate table", respectively.

25 In the present document, the invention is described using a reference system of orthogonal X, Y and Z directions and rotation about an axis parallel to the I direction is denoted Ri. Further, unless the context otherwise requires, the term "vertical" (Z) used herein is intended to refer to the direction normal to the substrate or mask surface or parallel to the optical axis of an optical system, rather than implying any particular orientation of the apparatus. Similarly, the term "horizontal" refers to a direction parallel to the substrate or mask surface or perpendicular to the optical axis, and thus normal to the "vertical" direction.

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Lithographic projection apparatus can be used, for example, in the manufacture of integrated circuits (ICs). In such a case, the mask (reticle) may contain a circuit pattern corresponding to an individual layer of the IC, and this pattern can be imaged onto an exposure area (die) on a substrate (silicon wafer) which has been coated with a layer of photosensitive material (resist). In general, a single wafer will contain a whole network of adjacent dies which are successively irradiated via the reticle, one at a time. In one type of lithographic projection apparatus, each die is irradiated by exposing the entire reticle pattern onto the die ^{at once} ~~in one go~~; such an apparatus is commonly referred to as a wafer stepper. In an alternative apparatus - which is commonly referred to as a step-and-scan apparatus - each die is irradiated by progressively scanning the reticle pattern under the projection beam in a given reference direction (the "scanning" direction) while synchronously scanning the wafer table parallel or anti-parallel to this direction; since, in general, the projection system will have a magnification factor M (generally < 1), the speed V at which the wafer table is scanned will be a factor M times that at which the reticle table is scanned. More information with regard to lithographic devices as here described can be gleaned from International Patent Application WO97/33205, for example.

Until very recently, lithographic apparatus contained a single mask table and a single substrate table. However, machines are now becoming available in which there are at least two independently moveable substrate tables; see, for example, the multi-stage apparatus described in International Patent Applications WO98/28665 and WO98/40791. The basic operating principle behind such multi-stage apparatus is that, while a first substrate table is at the exposure position underneath the projection system for exposure of a first substrate located on that table, a second substrate table can run to a loading position, discharge a previously exposed substrate, pick up a new substrate, perform some initial measurements on the new substrate and then stand ready to transfer the new substrate to the exposure position underneath the projection system as soon as exposure of the first substrate is completed; the cycle then repeats. In this manner it is possible to increase substantially the machine throughput, which in turn improves the cost of ownership of the machine. It should be understood that the same principle could be used with just one substrate table which is moved between exposure and measurement positions.

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In a lithographic apparatus the size of features that can be imaged onto the wafer is limited by the wavelength of the projection radiation. To produce integrated circuits with a higher density of devices, and hence higher operating speeds, it is desirable to be able to image smaller features. ^{which} ~~While~~ most current lithographic projection apparatus employ ultraviolet light generated by mercury lamps or excimer lasers, it has been proposed to use shorter wavelength radiation of around 13nm. Such radiation is termed extreme ultraviolet (EUV) or soft x-ray and possible sources include laser plasma sources or synchrotron radiation from electron storage rings. An outline design of a lithographic projection apparatus using synchrotron radiation is described in "Synchrotron radiation sources and condensers for projection x-ray lithography", JB Murphy et al, Applied Optics Vol. 32 No. 24 pp 6920-6929 (1993).

Optical elements for use in the EUV spectral region, e.g. multilayered thin film reflectors, are especially sensitive to physical and chemical damage which can significantly reduce their reflectivity and optical quality. Reflectivities at these wavelengths are already low compared to reflectors at longer wavelengths which is a particular problem since a typical EUV lithographic system may have nine mirrors; two in the illumination optics, six in the imaging optics plus the reflecting reticle. It is therefore evident that even a "small" decrease of 1-2% in the peak reflectivity of a single mirror will cause a significant light throughput reduction in the optical system.

A further problem is that some sources of EUV radiation, e.g. plasma based sources, are "dirty" in that they also emit significant quantities of fast ions and other particles which can damage optical elements in the illumination system.

Proposals to reduce these problems have involved maintaining the optical systems at very high vacuum, with particularly stringent requirements on the partial pressures of hydrocarbons which may be adsorbed onto the optical elements and then cracked by the EUV radiation to leave opaque carbon films.

SUMMARY OF THE INVENTION

It is an object of the present invention to provide optical elements, including multilayer mirrors, for use in lithographic projection apparatus using extreme

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According to the present invention, this and other objects are achieved in a lithographic projection apparatus comprising:

- 10 at least one optical element having a surface on which radiation of the same wavelength as the wavelength of said projection beam is incident and a capping layer covering said surface, said capping layer being formed of a relatively inert material.

The relatively inert material in particular should be resistant to oxidation and may be selected from the group comprising: diamond-like carbon (C), boron nitride (BN), boron carbide (B_4C), silicon nitride (Si_3N_4), silicon carbide (SiC), B, Pd, Ru, Rh, Au, MgF_2 , LiF, C_2F_4 and TiN and compounds and alloys thereof.

The capping layer may itself have a multi-layer structure, e.g. of two layers, with the outermost layer chosen both for improved chemical resistance and low refractive index at the wavelength of the projection beam to improve reflectivity or transmissivity.

A second aspect of the invention provides a device manufacturing method using a lithographic apparatus comprising

- an illumination system for supplying a projection beam of radiation;
a first object table provided with a first object holder for holding a mask;
a second object table provided with a second object holder for holding a substrate;
and
- 5 a projection system for imaging an irradiated portion of the mask onto a target portion of the substrate; said method comprising the steps of:
- providing a mask containing a pattern to said first object table;
providing a substrate at least partially covered by a layer of energy-sensitive material to said second object table;
- 10 irradiating said mask and imaging irradiated portions of said pattern onto said substrate; characterised in that:
- said lithographic projection apparatus comprises at least one optical element having a surface on which radiation of the same wavelength as the wavelength of said projection beam is incident and a capping layer covering said surface, said capping layer
- 15 being formed of a relatively inert material.
- In a manufacturing process using a lithographic projection apparatus according to the invention a pattern in a mask is imaged onto a substrate which is at least partially covered by a layer of energy-sensitive material (resist). Prior to this imaging step, the substrate may undergo various procedures, such as priming, resist coating and a soft
- 20 bake. After exposure, the substrate may be subjected to other procedures, such as a post-exposure bake (PEB), development, a hard bake and measurement/inspection of the imaged features. This array of procedures is used as a basis to pattern an individual layer of a device, e.g. an IC. Such a patterned layer may then undergo various processes such as etching, ion-implantation (doping) metallization, oxidation, chemo-mechanical
- 25 polishing, etc., all intended to finish off an individual layer. If several layers are required, then the whole procedure, or a variant thereof, will have to be repeated for each new layer. Eventually, an array of devices will be present on the substrate (wafer). These devices are then separated from one another by a technique such as dicing or sawing, whence the individual devices can be mounted on a carrier, connected to pins,
- 30 etc. Further information regarding such processes can be obtained, for example, from the book "Microchip Fabrication: A Practical Guide to Semiconductor Processing",

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Third Edition, by Peter van Zant, McGraw Hill Publishing Co., 1997, ISBN
0-07-067250-4.

Although specific reference may be made in this text to the use of the apparatus
5 according to the invention in the manufacture of ICs, it should be explicitly understood
that such an apparatus has many other possible applications. For example, it may be
employed in the manufacture of integrated optical systems, guidance and detection
patterns for magnetic domain memories, liquid-crystal display panels, thin-film magnetic
heads, etc. The skilled artisan will appreciate that, in the context of such alternative
10 applications, any use of the terms "reticle", "wafer" or "die" in this text should be
considered as being replaced by the more general terms "mask", "substrate" and "target
area", respectively.

4 **BRIEF DESCRIPTION OF THE ^{DRAWINGS} EMBODIMENTS**

The present invention and its attendant advantages will be described below with
15 reference to exemplary embodiments and the accompanying schematic drawings, in
which:

Fig. 1 depicts a lithographic projection apparatus according to the invention;

Fig. 2 is a graph of layer thicknesses in a 51 period optimised Mo/Si stack
according to the invention;

20 Fig. 3 is a graph of layer thicknesses in a 50 period Mo-Ru/Si stack according to
the invention;

Fig. 4 is a graph of R^9 vs. wavelength in the 13.4nm region for various mirrors
embodying the invention and a conventional mirror for comparison;

25 Fig. 5 is a graph of layer thicknesses in a 50 period Mo-Ru-Sr/Si stack according to
the invention;

Fig. 6 is a graph of layer thicknesses in a needle optimised 50 period Mo-Ru-Sr/Si
stack according to the invention;

Fig. 7 is a graph of layer thicknesses in an 80 period Ru-Sr/Be stack according to
the invention;

30 Fig. 8 is a graph of R^9 vs. wavelength in the 11.3nm region for various mirrors
embodying the invention and a conventional mirror for comparison;

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Fig. 9. Is a graph showing R^9 vs. wavelength for various stacks, both conventional and according to the invention as well as the emission intensity of a Xe-jet laser-induced plasma source;

Fig. 10 is a graph showing R and R^9 vs. wavelength for a Rh-Ru/Sr-Ce stack
5 according to the invention;

Fig. 11 is a graph of layer thicknesses in an optimised Rh-Ru/Sr-Ce stack according to the invention;

Fig. 12 is a graph showing R versus wavelength for a Rh-Ru/SiO₂-aero stack according to the invention; and

Fig. 13 is a diagram of a multilayer coating having a capping layer according to the
10 invention.

In the various drawings, like parts are indicated by like references.

a DETAILED DESCRIPTION OF THE EMBODIMENTS

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Embodiment 1

Figure 1 schematically depicts a lithographic projection apparatus according to the invention. The apparatus comprises:

- a radiation system LA, IL for supplying a projection beam PB of EUV radiation;
- 20 • a first object table (mask table) MT provided with a mask holder for holding a mask MA (e.g. a reticle), and connected to first positioning means PM for accurately positioning the mask with respect to item PL;
- a second object table (substrate table) WT provided with a substrate holder for holding a substrate W (e.g. a resist-coated silicon wafer), and connected to second
- 25 positioning means PW for accurately positioning the substrate with respect to item PL;
- a projection system ("lens") PL (e.g. a refractive or catadioptric system or a reflective system) for imaging an irradiated portion of the mask MA onto a target portion C (die) of the substrate W.

The radiation system comprises a source LA (e.g. an undulator or wiggler provided
30 around the path of an electron beam in a storage ring or synchrotron or a laser-induced plasma source) which produces a beam of radiation. This beam is passed along various optical components included in illumination system ("lens") IL so that the resultant

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beam PB is collected in such a way as to give uniform illumination at the entrance pupil and the mask.

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The beam PB subsequently impinges upon the mask MA which is held in a mask holder on a mask table MT. Having been selectively reflected by the mask MA, the beam PB passes through the lens PL, which focuses the beam PB onto a target area C of the substrate W. With the aid of first positioning means PW and the interferometric displacement measuring means IF, the substrate table WT can be moved accurately, e.g. so as to position different target areas C in the path of the beam PB. Similarly, the positioning means PM can be used to accurately position the mask MA with respect to the path of the beam PB, e.g. after mechanical retrieval of the mask MA from a mask library. In general, movement of the object tables MT, WT will be realised with the aid of a long stroke module (course positioning) and a short stroke module (fine positioning), which are not explicitly depicted in Figure 1.

The depicted apparatus can be used in two different modes:

- 15 • In step mode, the mask table MT is kept essentially stationary, and an entire mask image is projected ^{at once} ~~in one go~~ (i.e. a single "flash") onto a target area C. The substrate table WT is then shifted in the x and/or y directions so that a different target area C can be irradiated by the beam PB;
- In scan mode, essentially the same scenario applies, except that a given target area C is not exposed in a single "flash". Instead, the mask table MT is movable in a given direction (the so-called "scan direction", e.g. the x direction) with a speed v, so that the projection beam PB is caused to scan over a mask image; concurrently, the substrate table WT is simultaneously moved in the same or opposite direction at a speed $V = Mv$, in which M is the magnification of the lens PL (typically, $M = 1/4$ or $1/5$). In this manner, a relatively large target area C can be exposed, without having to compromise on resolution.

The illumination system IL may be constructed as described in copending European Patent Application 00300784.6 (applicant's ref P-0129), which is hereby incorporated by reference.

Examples

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The examples of the invention described below are obtained from computations performed using the thin film design program TFCalc (Software Spectra Inc.) and verified using LPro (4D Technology Ltd.). The built-in global and needle optimisation routines of TFCalc were used for the optimisation process, as described in A.V.

- 5 Tikhonravov, Appl. Opt. 32, 5417 (1993), A.V. Tikhonravov, M.K. Trubetskov and GM. DeBell, Appl. Opt. 35, 5493 (1996) and J.A. Dobrowski and R. A. Kemp, Appl. Opt. 29, 2876 (1990), which references are incorporated herein by reference. The optical constants of the various materials, namely the complex refractive index $N = n - ik$ are derived from atomic scattering factors by Henke et. al. and were obtained from the
- 10 CXRO web server at Berkeley (B. L. Henke, E. M. Gullikson, and J. C. Davis, Atomic Data and Nuclear Data Tables, 54(2), 181-342 (1993); http://www.cxro.lbl.gov/optical_constants/). The values of n and k for the materials used were downloaded as functions of wavelength from 6nm to 42nm and as such the wavelength dependence of n and k is implicit in all calculations. The values of n and k
- 15 for various materials at some wavelengths of particular interest are tabulated in Table 1 below. To demonstrate the performance enhancement of the reflectors according to the invention, we assume ideal "white" light illumination in the examples below.

Comparative Example 1

- 20 Comparative Example 1 is a standard Si-based multilayer stack comprising an unoptimised 50-period Mo/Si system grown on a Zerodur (RTM) glass substrate, with a partition ratio $\Gamma = 0.4$, yielding $d_{\text{Mo}} = 2.8\text{nm}$ and $d_{\text{Si}} = 4.1\text{nm}$. In addition, it is assumed that the final Si layer will undergo oxidation and effectively form a $\sim 2\text{nm}$ layer of native oxide. Analysis of such a stack yields a peak reflectivity at $\sim 13.4\text{nm}$ of $R = 0.731$. This
- 25 stack provides the reference for performance comparisons of stacks according to the invention.

Examples 2 to 23

- 30 Examples 2 to 23 according to the invention consist of variations on the stack of reference example 1 as detailed in Table 2 below. In Table 2, column 2 gives the materials used in the layers of the stack; column 3 gives the ^{optimization} optimisation applied: N indicates none, Y indicates global ^{optimization} optimisation and Y(n) indicates needle ^{optimization} optimisation

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(described further below); column 4 gives the capping layer applied; column 5 gives the peak reflectivity R ; column 6 gives the R° peak reflectivity in relative units and column 7 gives the R° int (integrated) reflectivity in relative units.

For a 9-reflector system, a more useful measure of optical throughput is the value of R° , which is the net reflectivity of a series of nine reflectors. R° int is the area under the curve in the R° vs. λ (wavelength) spectrum. The variation between R° peak and R° int for a given stack is an indication of the variation in the spectral half-width which is a function of the ~~optimisation~~ ^{optimization} process, or the incorporated materials, or the capping layer material, or any combination of the three.

The final surface layer of all of examples 2 to 20 is a 4.1-4.5nm Si layer on which the capping layer specified in column 4 is deposited, or grown in the case of SiO₂. Growing the SiO₂ consumes the surface Si layer so that in the case of Example 2 the top two layers are 2nm of Si, the remains of the approximately 4nm Si layer prior to oxidation and which may be regarded as the final layer of the multilayer, and 2nm SiO₂. Examples 21 to 23 are terminated with a 4.0 to 4.4nm Rb layer upon which the capping layer specified in column 4 is deposited.

Example 2 is an ~~unoptimized~~ ^{unoptimized} Mo/Si stack in which a 2nm native oxide is allowed to grow on a 6nm Si top layer (compared to the 4nm top layer of comparative example 1), resulting in a 1% increase in R , a 13% increase in R° peak and a 7% increase in R° int.

In example 3, a 25% gain in R° int is achieved by deposition of a 2nm B capping layer. Further increases in examples 4 to 7 follow by selecting Rh or Ru as capping layers and optimising the stack. A gain of up to 36% for a two-component (Mo/Si) multilayer stack can be achieved by ~~optimisation~~ ^{optimization}, as shown by example 7.

Figure 2 shows the layer structure of a 51 period (102 layer) ~~optimized~~ ^{optimized} Mo/Si stack with a 1.5nm capping layer. In the Figure, layer 0 is the substrate surface. As can be seen, the optimisation of the Mo/Si stack results in a gradual, smooth variation of the layer thicknesses through the stack while the period width remains nominally constant at about 6.8 to 7.0nm. Near the substrate, $d_{\text{Mo}} \approx d_{\text{Si}} \approx 3.5\text{nm}$ varying to $d_{\text{Mo}} \approx 2.7\text{nm}$ and $d_{\text{Si}} \approx 4.2\text{nm}$ near the surface. In the stack illustrated in Figure 2 the partition ratio Γ remains at about 0.4 for the first 20 periods from the surface (one period = one pair of layers, i.e. one Mo layer and one Si layer) and thereafter gradually changes to about 0.5 at the substrate. Thus it appears that the higher the absorption in the material, the

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lower the thickness near the surface, for an optimum reflectivity response. This phenomenon is discussed further below.

The three component system of examples 8 to 12 is set up initially as a two-component Mo/Si stack with the third material interleaved between the Mo and Si layers with its initial thickness set to zero. The global ^{optimization} ~~optimisation~~ process then varies the thicknesses of all the layers until a pre-set reflectivity target is approached. In the case of Mo-Rh/Si and Mo-Ru/Si, Mo is ^{favored} ~~favoured~~ near the surface and Rh or Ru near the substrate whereas, in the Mo-RbCl/Si system, RbCl (which is a single entity) partially substitutes for Si in the centre of the stack, i.e. the sum of the thicknesses of the adjacent RbCl and Si layers approaches the thickness of Si in a standard stack. The layer structure for the Mo-Ru/Si stack is shown in Figure 3. This stack has 50 Si layers, including the uppermost layer, and therefore has 148 layers in total, plus a 1.5nm Ru capping layer. In the figure, layer 0 is the substrate surface. A 50% gain in computed throughput is observed for the Mo-Ru/Si system over the standard Mo/Si stack.

Example 12 shows a further improvement in R°_{int} for the Mo-Ru/Si system using needle ^{optimization} ~~optimisation~~. In the needle ^{optimization} ~~optimisation~~ routine, additional layers of designated materials, in this case, Mo, Ru and Rh, with vanishingly small thicknesses, are periodically added to the stack. These layers are then allowed to grow or be rejected by a local ^{optimization} ~~optimisation~~ process. The needle ^{optimized} ~~optimised~~ stack therefore also contains Rh and additional layers of Mo, the net result of which is a 59% increase in R°_{int} compared to the standard stack. It is also worth noting that in this case $R^{\circ}_{int} > R^{\circ}_{peak}$ with the peak reflectivity of 0.764 only marginally lower than for the standard ^{optimized} ~~optimised~~ Mo-Ru/Si stack. This indicates that a substantially greater spectral half-width results from the needle ^{optimization} ~~optimisation~~ process as can be seen in Fig. 4, which is a graph showing R° vs. wavelength in the 13.4nm region. Line A is for the standard Mo/Si stack, reference example 1; B is ^{optimized} ~~optimised~~ Mo/Si, example 4; C is Mo-Ru/Si needle ^{optimized} ~~optimised~~, example 12; D is Mo-Ru-Sr/Si needle ^{optimized} ~~optimised~~, example 19, and E is Mo/Rb ^{optimized} ~~optimised~~, example 22.

The order of layers in the three component stacks may be varied. For example, Rh-Mo/Si may be used instead of Mo-Rh/Si and Ru-Mo/Si instead of Mo-Ru/Si

The four-component stacks, examples 13 to 20, were built in a similar manner to the three component stacks described above. The most favourable combination is Mo-

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Reference Example 24

Reference example 24 is a multilayer stack for use at 11.3nm comprising an unoptimised 80-period Mo/Be system grown on a Zerodur (RTM) glass substrate, with a partition ratio $\Gamma = 0.4$ yielding $d_{\text{Mo}} = 2.3\text{nm}$ and $d_{\text{Be}} = 3.4\text{nm}$. This provides the reference for examples 25 to 40 which are tuned for use at 11.3nm.

Examples 25 to 40

Table 3 corresponds to Table 2 but gives data for examples 25 to 40 according to the invention which are reflector stacks tuned for use at 11.3nm.

a The effects of ~~optimisation~~^{optimization} and the capping layer deposition are less important at 11.3nm than at 13.4nm, only 8% improvement in R^{int} is provided.

However, Ru and Rh are preferred to Mo for the 11.3nm window. The Ru/Be stack has a relative optical throughput greater by up to 70% compared to the Mo/Be reference example, whilst the throughput of the Rh/Be stack is 33% greater. Although this is significantly lower than for Ru/Be, this combination may be preferable in some applications of the invention due to factors such as Rh-Be interface chemistry.

a A particularly preferred embodiment of the invention is the "needle" ~~optimized~~^{optimization} Rh/Be stack which exhibits a huge increase in reflectivity. This is due to the incorporation of Pd, Ru and Mo layers during the ~~optimisation~~^{optimization} process effectively transforming it into a Rh-Ru-Pd-Mo/Be or Pd-Rh-Ru-Mo/Be multi-component stack.

a 20 The layer thicknesses of an 80 period (80 Be layers) Ru-Sr/Be stack capped with a 1.5nm Ru layer are shown in Figure 7. Similar results may be achieved with Ru/Sr-Be. As before, the substrate surface is indicated at layer 0. Due to their similar optical constants, Be and Sr perform similar functions in the stack with Ru predominating near the substrate. The sum of the Be and Sr thicknesses near the surface is about 4.1nm whilst the Ru thickness is about 1.7nm. These are markedly different than the thicknesses of the Mo/Be stack with $\Gamma = 0.4$. This is because of the higher extinction coefficient of Ru, as compared to Mo, such that a lower Ru thickness is preferred. The gain in employing Ru in place of Mo derives from the resultant increase in optical contrast with Be. The preferred stack period is: Ru-Sr-Be.

Selected spectra of Be-based multilayers are shown in Fig. 8. This Figure shows plots of R^9 vs. wavelength in the 11.3nm region for five stacks. A is the reference Mo/Be stack, B is an optimised Mo/Be stack with a Ru capping layer, C is an optimised Ru/Be stack, D is a needle optimised Rh/Be stack and E is an optimised, Ru-capped Ru-Sr/Be stack.

Examples 35 to 40 are strontium-containing three component systems which yield throughput enhancements of up to a factor of 2.

As capping layers, Rh and Ru are optimum for this wavelength region and give an increase of 0.7- 1.0% in R.

Examples 41 to 44

From the above computational analysis of the various multilayer systems for the EUV region between 11nm and 14nm it would appear that significant enhancements in peak reflectivities and the integrated reflectivities for a 9-mirror optical system are possible. A combination of capping layer choice, global and needle optimisation routines and, most importantly, the incorporation of additional or replacement materials within the stack appears to be the recipe for reflectivity enhancement. Metals such as Rh and Ru which are generally easily deposited using various vacuum deposition techniques provide advantages, especially in conjunction with Be for the 11.3nm region where they surpass Mo in theoretical performance. Furthermore, it is conceivable that using the various combinations discussed above, problems of interface roughness associated with Mo/Si(Be) may be alleviated somewhat.

In for instance the Mo-Rh/Si and Mo-Ru/Si stacks, improved results are provided with Rh(Ru) predominating over Mo near the substrate and vice-versa near the surface.

This may be because at 13.4nm Rh and Ru exhibit a higher optical contrast with Si than does Mo whereas the extinction coefficient k , and therefore the absorption within the layer, is lower for Mo than Rh and Ru. Near the surface of the stack, it is important that there be low absorption so that the incident radiation penetrates as deep into the stack as possible so that the phasor addition is ^{maximised} ~~maximised~~. However, deep within the stack where the intensity is low, increased optical contrast is ^{favoured} ~~favoured~~ for the reflected intensity to be ^{maximised} ~~maximised~~.

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When Sr is incorporated in the structure it is preferentially located in the near-surface region of the stack and partially substitutes Si. This can be explained by similar arguments, the value of n for Sr is lower than that of Si and therefore ~~while~~^{whilst} the optical contrast with the low- n materials is slightly lowered, the lower value of k for Sr compared with Si (see Table 1) means that the absorption within the layer is lower thus ~~favoring~~^{favoring} Sr near the surface of the stack. The data obtained for Be-based stacks for 11.3nm operation indicates that similar effects occur.

Examples 41 to 44 are designed for use with a Xenon-jet laser-induced plasma source (Xe-Jet LPS) which has a peak output intensity at about 10.9nm, somewhat lower than the range for which the reflectors described above were designed.

Figure 9 shows the R^0 reflectivities (left axis) of various reflectors and the relative Xe-Jet LPS emission intensity (right axis) vs. wavelength in nm (X axis). In Figure 9:

- (a) is the spectral response of the conventional ~~unoptimised~~^{unoptimised} Mo/Si stack and is used as the reference for relative reflectivity figures.
- (b) is an ~~optimised~~^{optimised} Mo/Si stack similar to example 7 above;
- (c) is an ~~optimised~~^{optimised} Rh-Ru-Mo/Sr-Si stack;
- (d) is a conventional, ~~unoptimised~~^{unoptimised}, Mo/Be stack similar to comparative example 24 above;
- (e) is an ~~optimised~~^{optimised} Rh-Mo/Be stock similar to example 40 above;
- (f) is an ~~optimised~~^{optimised} Pd-Rh-Ru-Mo/Be stack;
- (g) is an ~~optimised~~^{optimised} Pd-Rh-Ru/RbCl stack forming example 41 of the invention;
- (h) is an ~~optimised~~^{optimised} Rh-Ru/P stack forming example 42 of the invention; and
- (i) is an ~~optimised~~^{optimised} Rh-Ru/Sr stack forming example 43 of the invention.

Although examples 41 to 43 have lower R^0 peak and R^0 int than other examples described above, they have the advantage of providing their peak reflectivity very close to the emission maximum of the Xe-Jet LPS. They are thus ideal for use with this source. Taking the throughput of the unoptimised Mo/Si stack as 1.0, examples 41(g), 42(h) and 43(i) provide relative throughputs of 3.0, 5.7, and 6.5 respectively. This also compares well with the throughput of the Mo/Be stack (d), which is 5.7 and avoids the use of Be, which is highly toxic.

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Further improvements in peak reflectivity, giving values greater than 0.75 in the 9.0 to 12nm region can be attained in four component stacks that combine P and Sr, e.g. Rh-Ru/P-Sr.

- a A further advance is shown by example 44. Example 44 is a needle ^{optimised} ~~optimised~~ Rh-
 5 Ru/Sr-Ce stack with a peak reflectivity of $R=0.776$ at 10.9nm. Figure 10 shows the full wavelength dependence of R (left axis) and R^9 (right axis) of example 44 in the 10 to 12nm range. Figure 11 shows layer thicknesses in this stack.

Examples 45 to 48

- 10 Some further alternative stack configurations are shown in Table 4. In this table, Example 45 is a three layer stack of Ru-Nb/Si, which demonstrates that Niobium can also give improvements in an Si-based stack, but is otherwise the same as the examples 8 to 12 of Table 2.

- a For use at 12.8nm, different multilayers may be optimal. Two such multilayers
 15 are example 47 and 48 of Table ⁴ ~~5~~. At 46, the R value of a conventional Mo/Si (equivalent to Comparative Example 1) at 12.8nm is given. It can readily be seen that
 a the addition of Ru partially replacing Mo improves reflectivity at this frequency ^{while} ~~whilst~~
 the use of beryllium as a spacer material partially replacing silicon provides further improvements.

- 20 In general, the lanthanides (rare earth metals) may provide good optical contrast with metals such as Mo, Ru and Rh and may be preferred in reflectors nearer the substrate. In this position, optical contrast is provided because the lanthanides have a refractive index n very close to unity which outweighs the disadvantage that their values of extinction coefficient k are not as low as some other materials in the 9-16nm region.
 25 Lanthanum is particularly preferred at or near 13nm.

- Further alternative spacers useable in the invention are porous materials such as low density (porous) silica (aerogel) having a density about 1 tenth that of bulk silica. Figure 12 shows the wavelength sensitivity of a Rh-Ru/SiO₂-aero stack using such porous silica. Its relatively broad reflectance peak below 11nm will be noted. Other
 30 low density materials that may be used include: titania and alumina aerogels; nanoporous silicon, meso-porous silicon, nanoclusters of silicon and other semiconductors. These materials may be used to manufacture reflectors tuned to specific wavelengths

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throughout the 8 to 20nm wavelength range. The materials are useful because the values on n and k are density dependent. With decreasing density the refractive index, n , tends to unity and the extinction coefficient, k , tends to zero. The density of a typical Si aerogel is 0.2gcm^{-3} whilst that of porous Si is 1.63gcm^{-3} .

5

Examples 49 to 65

Further examples of useful capping layers are set out in Tables 5 and 6, which give the same data as previous tables.

9 In Table 5, 49 is a comparative example consisting of an ^{optimised} (for 13.4nm) 50
10 period Mo/Si stack whose outermost layer is 2nm of SiO_2 formed by natural oxidation
of the final Si layer in the stack. This comparative example forms the reference for
relative values of R°_{peak} and R°_{int} for Examples 50 to 57 of the invention. These
examples differ from comparative example 49 only in the indicated capping layer, which
9 is deposited on final Si layer of the stack before that layer can ^{oxidise} ~~oxidise~~. It will be seen
15 that each of palladium (Pd), boron carbide (B_4C), boron nitride (BN), silicon carbide
(SiC), silicon nitride (Si_3N_4) and diamond-like carbon (dl-C) exhibit improved
reflectance, or an acceptable reduction, whilst exhibiting a high degree of resistance to
chemical attack.

4 In Table 6, 58 is a comparative example consisting of an 80 period ^{optimised} (for
20 11.3nm) Mo/Be stack, similarly with an outermost layer of 2nm BeO formed by natural
oxidation of the final Be layer. This comparative example forms the reference for the
relative values of R°_{peak} and R°_{int} for Examples 59 to 65 of the invention. Examples 59
to 65 differ from comparative example 58 in the indicated capping layer which is
9 deposited before the outer Be layer can ^{oxidise} ~~oxidise~~. It will again be seen that the layers
25 specified provide improved reflectivity, or an acceptable reduction, whilst exhibiting a
high degree of resistance to chemical attack.

Examples 66 to 76

30 In examples 66 to 76 the capping layer includes a modified final layer of the
multilayer coating as well as a dedicated capping sub-layer so as to form a bi- or tri-layer
protective structure thus increasing the overall thickness of the top layers and reducing

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the likelihood of incomplete coverage through multiple layer deposition. This is illustrated in Figure 13.

The reflector of examples 66 to 76 of the invention comprises substrate 10 on which are deposited N periods of alternating layers of a first material 11 and a second material 12. In Figure 13 only the first period 13 is shown however all periods save the last are similar. The final, Nth period comprises a layer 15 of the first material, a layer 16 of a third material and a capping sub-layer 17 of a capping material. In the following, the first material is denoted X, the second material Y and the third material Z.

The first material X is one or more of: Mo, Ru, Rh, Nb, Pd, Y and Zr, and the second material Y is one or more of: Be, Si, Sr, Rb, RbCl and P. The final period is constructed such that the substance X is chosen as previously, the third material Z on the other hand, is chosen from a set of materials with a moderately high value of refractive index n (> 0.96), sufficiently low value of the extinction coefficient k (< 0.01), and which are known for their chemical inertness and stability. For the 10-15 nm spectral region the following materials are suitable: B₄C, BN, diamond-like C, Si₃N₄ and SiC. Although these materials are not ideal "spacers", the reflectivity loss through absorption in layer 16 may be tolerated in favour of long-term chemical and structural integrity of the multilayer. In addition, the combination of layers 15 and 16 has a total optical thickness of ~ 2 quarter wavelengths (where the quarter-wave optical thickness is given by: $QW = 4nd/\lambda$), thus contributing to the reflection coefficient and avoiding a drastic reduction in the reflectivity which may be caused by relatively thick ($> 3\text{nm}$) capping layers. In addition the material of the capping layer 17 has low n such that a large optical contrast is maintained between layers 16 and 17. The boundary between layers 16 and 17 also serves to localise the node of the standing wave formed through the superposition of the incident and reflected waves. Suitable materials for capping layer 17 in this configuration are: Ru, Rh, Pd and diamond-like C.

Table 7 shows layer materials and thicknesses for Examples 66 to 71 which comprise 79 periods of Mo/Be plus the additional period X/Z constructed as described above. These examples are intended for use at 11.3nm. In example 66, the whole of the Be layer is oxidized and a Ru capping layer is deposited. This is the reference example. Example 67 shows that SiC is not ideal for the 11.3 nm region. However, Examples 70 and 71 show clearly that values of R greater than 75.5% are still possible with such a

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configuration. Rh is used to replace the Mo layer on account of its inertness and C or B₄C is deposited as layer 16 with an additional coating of Ru as layer 17. This gives a tri-layer of thickness of 7.7 nm forming the protective coating structure. Examples 68 and 69 are analogous to 70 and 71 respectively, with the important distinction that the thickness of the layer 17 is increased by 2QW resulting in lower, but still respectable, reflectivity values and with a substantially higher tri-layer thickness of 13.7 nm.

Similarly, Table 8 shows layer materials and thicknesses for Examples 72 to 76 which comprise 49 periods of Mo/Si with the additional period formed by the X/Z combination again terminated with a Ru capping layer. The reference example 72 represents a fully oxidised top Si layer upon which a Ru capping layer is applied. SiC and B₄C are the most favorable materials for the Z layer 16. However, at 13.4, for which these examples are intended, Mo cannot be replaced by the more inert metal Rh, therefore a bi-layer protective structure is formed where the combined thickness of layers 16 and 17 ($d_z + d_{cl}$) is about 5.5-6.0 nm. In example 73 the thickness of the SiC layer is increased by 2QW resulting in a 12.6 nm protective bi-layer thickness at the expense of reflectivity.

Other suitable materials for the capping layer are Au, MgF₂, LiF, C₂F₄ (teflon) and TiN

9 20 ^{while}~~Whilst~~ we have described above specific embodiments of the invention it will be appreciated that the invention may be practiced otherwise than as described. The description is not intended to limit the invention.

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	10.9nm		11.3nm		13.4nm	
	n	k	n	k	n	k
B			0.9786	0.0023	0.9689	0.0040
B ₄ C			0.9753	0.0029	0.9643	0.0050
Be	1.0092	0.0196	1.0081	0.0010	0.9892	0.0018
BeO			0.9785	0.0102	0.9587	0.0171
BN			0.9740	0.0050	0.9633	0.0086
C			0.9732	0.0040	0.9622	0.0067
Ce	1.0522	0.0197	1.0380	0.0159	1.0074	0.0062
Eu	0.9902	0.0062	0.9883	0.0074	0.9812	0.0123
La	1.0777	0.0601	1.0460	0.0200	1.0050	0.0065
Mo			0.9514	0.0046	0.9227	0.0062
P	0.9949	0.0014				
Pd	0.9277	0.0099	0.9198	0.0135	0.8780	0.0443
Pr	1.0167	0.0119	1.0115	0.0125	0.9840	0.0072
Rb			0.9974	0.0014	0.9941	0.0007
RbCl	0.9943	0.0023	0.9941	0.0022	0.9895	0.0019
Rh	0.9313	0.0068	0.9236	0.0089	0.8775	0.0296
Ru	0.9373	0.0056	0.9308	0.0063	0.8898	0.0165
Si			1.0055	0.0146	0.9999	0.0018
Si aerogel	0.9988	0.0011				
Porous Si	1.0015	0.0049				
Si ₃ N ₄			0.9864	0.0173	0.9741	0.0092
SiC			0.9936	0.0159	0.9831	0.0047
SiO ₂			0.9865	0.0123	0.9787	0.0106
Sr	0.9936	0.0011	0.9928	0.0011	0.9880	0.0013
Y			0.9835	0.0020	0.9742	0.0023
Zr			0.9733	0.0029	0.9585	0.0037

Table 2

				R	R ^o _{peak}	R ^o _{int}
1	Mo/Si	N	2nm SiO ₂	0.731	1.00	1.00
2	Mo/Si	N	(2nm Si +) 2nm SiO ₂	0.741	1.13	1.07
3	Mo/Si	N	2nm B	0.751	1.27	1.25
4	Mo/Si	Y	2nm B	0.752	1.29	1.26
5	Mo/Si	Y	1.5nm Rh	0.754	1.32	1.27
6	Mo/Si	N	1.5nm Ru	0.757	1.37	1.35
7	Mo/Si	Y	1.7nm Ru	0.758	1.39	1.36
8	Mo-Rh/Si	Y	1.7nm Ru	0.762	1.45	1.38
9	Mo-RbCl/Si	Y	1.5nm Ru	0.761	1.44	1.39
10	Mo-Ru/Si	Y	1.5nm Rh	0.760	1.42	1.41
11	Mo-Ru/Si	Y	1.7nm Ru	0.765	1.51	1.50
12	Mo-Ru/Si	Y(n)	1.5nm Ru	0.764	1.48	1.59
13	Mo-Rh-RbCl/Si	Y	1.7nm Ru	0.764	1.49	1.38
14	Mo-Ru-Zr/Si	Y	1.7nm Ru	0.764	1.49	1.44
15	Mo-Ru-Y/Si	Y	1.5nm Ru	0.770	1.60	1.55
16	Mo-Ru-RbCl/Si	Y	1.5nm Ru	0.767	1.54	1.56
17	Mo-Rh-Sr/Si	Y	1.6nm Ru	0.779	1.77	1.56
18	Mo-Ru-Sr/Si	Y	1.5nm Rh	0.776	1.71	1.57
19	Mo-Ru-Sr/Si	Y	1.5nm Ru	0.791	1.81	1.68
20	Mo-Ru-Sr/Si	Y(n)	1.5nm Ru	0.781	1.81	1.85
21	Ru/Rb	Y	1.5nm Ru	0.779	1.77	1.41
22	Mo/Rb	Y	1.5nm Ru	0.809	2.49	2.13
23	Mo-Ru-Sr/Rb	Y	1.5nm Ru	0.814	2.63	2.20

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Table 3

				R	R ^o _{peak}	R ^o _{int}
24	Mo/Be	N	None	0.775	1.00	1.00
25	Mo/Be	N	1.5nm Rh	0.782	1.08	1.08
26	Mo/Be	Y	None	0.780	1.06	1.00
27	Mo/Be	Y	1.5nm Rh	0.787	1.15	1.06
28	Mo/Be	Y	1.5nm Ru	0.788	1.16	1.08
29	Ru/Be	Y	1.5nm Rh	0.810	1.49	1.68
30	Ru/Be	Y	1.5nm Ru	0.811	1.50	1.70
31	Rh/Be	N	1.5nm Rh	0.793	1.10	1.33
32	Rh/Be	Y	1.5nm Rh	0.793	1.23	1.29
33	Rh/Be	Y	1.5nm Ru	0.794	1.24	1.31
34	Rh/Be	Y(n)	1.5nm Rh	0.811	1.50	1.77
35	Mo-Sr/Be	Y	1.5nm Rh	0.799	1.32	1.21
36	Ru-Sr/Be	Y	1.5nm Rh	0.822	1.70	1.97
37	Ru-Sr/Be	Y	1.5nm Ru	0.823	1.72	2.00
38	Rh-Sr/Be	Y	1.5nm Rh	0.810	1.49	1.64
39	Rh-Sr/Be	Y	1.5nm Ru	0.811	1.50	1.67
40	Ru-Mo/Be	Y(n)	1.5nm Ru	0.812	1.52	1.72

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				R	R ⁹ peak	R ⁹ int
45	Ru-Nb/Si	Y	2nm Rh	0.754	1.20	1.27
46	Mo/Si	N	2nm Si + 2nm SiO ₂	0.738	1.00	1.00
47	Ru-Mo/Si	Y	2nm Rh	0.768	1.43	1.48
48	Ru-Mo/Be-Si	Y	2nm Rh	0.778	1.61	1.63

				R	R [°] _{peak}	R [°] _{int}
49	Mo/Si	Y	2nm SiO ₂	0.745	1.00	1.00
50	Mo/Si	Y	2nm Pd	0.743	0.97	0.92
51	Mo/Si	Y	2nm Si ₃ N ₄	0.747	1.01	1.02
52	Mo/Si	Y	2nm SiC	0.748	1.03	1.04
53	Mo/Si	Y	2nm BN	0.749	1.04	1.05
54	Mo/Si	Y	2nm Rh	0.751	1.06	1.05
55	Mo/Si	Y	2nm (dl-)C	0.750	1.06	1.08
56	Mo/Si	Y	2nm B ₄ C	0.751	1.07	1.10
57	Mo/Si	Y	2nm Ru	0.758	1.16	1.17

				R	R [°] _{peak}	R [°] _{int}
58	Mo/Be	Y	2nm BeO	0.774	1.00	1.00
59	Mo/Be	Y	2nm SiC	0.769	0.94	0.92
60	Mo/Be	Y	2nm BN	0.779	1.06	1.09
61	Mo/Be	Y	2nm Pd	0.781	1.09	1.10
62	Mo/Be	Y	2nm (dl)-C	0.781	1.08	1.11
63	Mo/Be	Y	2nm B ₄ C	0.782	1.09	1.13
64	Mo/Be	Y	2nm Rh	0.786	1.15	1.18
65	Mo/Be	Y	2nm Ru	0.788	1.17	1.21

Table 7

	X/Y	X	Z	CL	R	R [°] _{peak}	R [°] _{int}
66	Mo/Be	2.05nm (0.69QW) Mo	3.77nm (1.31QW) BeO	2.03nm Ru	0.717	1.00	1.00
67	Mo/Be	4.12nm (1.35QW) Rh	1.93 nm (0.68QW) SiC	2.04nm Ru	0.713	0.95	0.91
68	Mo/Be	1.70nm (0.56QW) Rh	9.95nm (3.43QW) C	2.03nm Ru	0.721	1.05	1.09
69	Mo/Be	1.56nm (0.51QW) Rh	10.06nm (3.47QW) B ₄ C	1.96nm Ru	0.739	1.30	1.25
70	Mo/Be	1.70nm (0.56QW) Rh	4.15nm (1.43QW) C	1.90nm Ru	0.756	1.61	1.57
71	Mo/Be	1.56nm (0.51QW) Rh	4.27nm (1.47QW) B ₄ C	1.85nm Ru	0.765	1.78	1.73

5 Table 8

	X/Y	X	Z	CL	R	R [°] _{peak}	R [°] _{int}
72	Mo/Si	2.84nm (0.78QW) Mo	4.24nm (1.24QW) SiO ₂	2.05nm Ru	0.699	1.00	1.00
73	Mo/Si	3.28nm (0.90QW) Mo	10.63nm (3.12QW) SiC	2.06nm Ru	0.696	0.97	0.93
74	Mo/Si	3.87nm (1.07QW) Mo	3.38nm (0.97QW) C	1.97nm Ru	0.716	1.24	1.21
75	Mo/Si	3.23nm (0.89QW) Mo	3.95nm (1.14QW) B ₄ C	1.92nm Ru	0.725	1.39	1.36
76	Mo/Si	3.28nm (0.90QW) Mo	3.82nm (1.12QW) SiC	1.87nm Ru	0.735	1.57	1.53

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